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# APPLICABILITY TABLE

## PRODUCTS

- ME910C1-NA
- ME910C1-NV
- ME910C1-J1
- ME910C1-AU
- ME910C1-K1
- ME910C1-E1
- ME910C1-WW
- ME910C1-E2
- NE910C1-E1
- NE910C1-NA
- ML865C1-EA
- ML865C1-NA
5.6. Hardware application example .................................................... 19
6. GLOSSARY AND ACRONYMS ................................................. 21
7. DOCUMENT HISTORY .............................................................. 22
1. INTRODUCTION

1.1. Scope
The ME910C1 includes unique advanced features in order to support the PSM according to 3GPP Rel-12.
The aim of this document is the description of the suggested Application design to use this functionality.

1.2. Audience
This document is intended for Telit customers, who are integrators, about to implement their applications using our ME910C1 modules.

1.3. Contact Information, Support
For general contact, technical support services, technical questions and report documentation errors contact Telit Technical Support at:

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- TS-APAC@telit.com

Alternatively, use:
http://www.telit.com/support

For detailed information about where you can buy the Telit modules or for recommendations on accessories and components visit:
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Our aim is to make this guide as helpful as possible. Keep us informed of your comments and suggestions for improvements. Telit appreciates feedback from the users of our information.
1.4. Text Conventions

**Danger** – This information MUST be followed or catastrophic equipment failure or bodily injury may occur.

**Caution or Warning** – Alerts the user to important points about integrating the module, if these points are not followed, the module and end user equipment may fail or malfunction.

**Tip or Information** – Provides advice and suggestions that may be useful when integrating the module.

All dates are in ISO 8601 format, i.e. YYYY-MM-DD.

1.5. Related Documents

- ME910C1 HW User Guide, 1VV0301210
- ML865C1 HW User Guide, 1VV0301493
- ME910C1 AT Commands Reference Guide, 80471ST10691A
- Telit EVK2 User Guide, 1vv0300704
2. OVERVIEW

The aim of this document is the description of some hardware solutions useful for developing a product with the Telit ME910C1 module.

In this document all the basic functions of a mobile phone will be taken into account; for each one of them a proper hardware solution will be suggested and eventually the wrong solutions and common errors to be avoided will be evidenced. Obviously this document cannot embrace the whole hardware solutions and products that may be designed. The wrong solutions to be avoided shall be considered as mandatory, while the suggested hardware configurations shall not be considered mandatory, instead the information given shall be used as a guide and a starting point for properly developing your product with the Telit ME910C1 module. For further hardware details that may not be explained in this document refer to the Telit ME910C1 Product Description document where all the hardware information is reported.

NOTICE:

EN) The integration of the LTE ME910C1 cellular module within user application shall be done according to the design rules described in this manual.

(IT) L'integrazione del modulo cellulare LTE ME910C1 all'interno dell'applicazione dell'utente dovrà rispettare le indicazioni progettuali descritte in questo manuale.

(DE) Die Integration des ME910C1 LTE Mobilfunk-Moduls in ein Gerät muß gemäß der in diesem Dokument beschriebenen Konstruktionsregeln erfolgen.

(SL) Integracija LTE ME910C1 modula v uporabniški aplikaciji bo morala upoštevati projektna navodila, opisana v tem priročniku.

(SP) La utilización del modulo LTE ME910C1 debe ser conforme a los usos para los cuales ha sido diseñado descritos en este manual del usuario.

(FR) L’intégration du module cellulaire LTE ME910C1 dans l’application de l’utilisateur sera faite selon les règles de conception décrites dans ce manuel.

(HE) המודול ה- ME910C1 LTE של תלייט ת_statuses לינקagement בỨ_ aplikacj的儿子 יוכלו להימצא בתוכנה של ה-ME910C1 תלייט

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3. PSM DESCRIPTION

3.1. PSM Procedure Overview

The Power Saving Mode (PSM) in 3GPP Rel12 allows the Module to skip idle mode tasks for a longer time period while still maintaining the NAS context. The functionality is available on M1/NB1 on the ME910C1 / NE910C1 Series.

This feature permits to reduce the overall power consumption when there is no required data activity with the network for a long time.

This saves the power also related to the Paging activity.

The PSM reduces the signaling load between the ME910C1 and the network on NAS level (24.301 Rel.12 chapter 5.3.11) compared to a standard attach/detach procedure.

Within the attach/RAU/TAU procedure the UE indicates that it supports PSM and the network confirms/accepts PSM usage by sending two different timers (T3324 and T3412 extended Value) in the confirmation message.

The timer T3324 specifies an active period after the RAU/TAU procedure the UE has to follow the normal idle mode procedures (paging reception, measurements,..).

After timer T3324 expires the Module enters PSM state, i.e. it disables all AS/NAS activities until the next periodic RAU/TAU update.

Timer T3412 extended value is defining the time between two subsequent RAU/TAU procedures and starts together with T3324. This implies that the time in which the module will be NOT reachable by the network (inactivity period) is given by T3412_ext - T3324.

Before the inactivity period starts the complete NAS context needs to be stored and reused when accessing the network again.

The Module can leave the PSM mode at any point in time when there is MO data or when periodic TAU timer expires.

The PSM is only intended for those Modules that can tolerate a high MT Call latency.

The 3GPP standard does not specify current limits to be satisfied or power reduced to when PSM is used by the module. Only the signaling reduction (i.e. Not doing a reattach but just a RAU/TAU procedure) is defined.
3.2. PSM for ME910C1

ME910C1 implements PSM features and allows the user to activate PSM by sending the specific AT command AT+CPSMS as described in [x] AT Command User Guide. As soon as PSM has been accepted by the network (i.e., timers have been received in TAU Accept message) T3324 starts and ME910 is in IDLE state with default module functionality. Since default functionality for ME910 (and all Telit modules) is CFUN=1 the current absorption for the module will be equal to standard idle CFUN=1 state and around 10-11 mA.

As T3324 expires the module enters the PSM state which is basically an OFF state with RTC running in the background bringing the current consumption level to around 8,5 μA.

Users willing to decrease the power consumption during T3324 can combine AT+CFUN states (e.g. AT+CFUN=5 and Asserting low the DTR pin) with PSM as shown in the below figures.

When AT+CFUN=5 is used during T3324 the specific functionality allows to save current still keeping module synchronized and reachable the network.

![PSM activation and CFUN=1 (default)](image-url)
An example of command to activating PSM for ME910 is as follows:

AT+CPSMS=1,,"," & T3412 & "," & T3324  
T3412= 10000011 -> 90 sec  
T3324= 00001010 -> 20 sec  

With these settings the module will send TAU every 90 sec and will stay IDLE for 20 sec. This is true when using a network simulator because in a real environment the final decision of which timers have to be applied is taken by the network that can accept the proposed timers or decide to send its own timers. If different timers are sent back by the network then DUT must apply the Network timers.

The nature of PSM and the current consumption profile suggest that the major efficiency is achieved when T3412 is longer than 5-6 hours. If an application cannot support modules being out of connection for so long but it is still willing to reduce power then eDRX feature should be evaluated.
Telit implementation of PSM includes a SW check that avoids UE to enter PSM mode if the settings of T3412 and T3324, are such that the next wake up would happen before a preconfigured minimum time duration 60s.

**As a result of this check the following precondition will need to be verified for the PSM to be activated :** \( T3412 - T3324 > 60 \text{ sec} \)

The above is to avoid an incorrect use of PSM resulting in higher current consumption due to shut down and reboot compared to the current consumption in idle state in 60s.
4. EXTENDED DRX (EDRX)

4.1. eDRX Procedure Overview

extended DRX (eDRX) is an extension of the discontinuous reception (DRX). DRX is a technic used during RRC IDLE to reduce UE power consumption that periodically listen to the paging channel and sleep in between two different paging listening events.

eDRX tries to enhance the power consumption increasing the sleeping period, but this has to be done in coordination with the network that will know about this extension and will cache paging requests directed to the UE.

eDRX features defines to different timers

**Paging Time Window (PTW)**: the window in which the UE will behaves in DRX mode

**eDRX cycle**: the time between the start of two different and subsequent time windows

these two timers are exchanged between UE and Network by means of Attach or TAU/RAU Accept message.

When eDRX is activated by means of the specific command the DRX activity is stopped for a longer period and the module remains in sleep and not listening the paging channel anymore till the end of the eDRX cycle. In other words the UE will not be reachable from the network from the end of the PTW to the end of the eDRX cycle.

Below is a pictures that explain the differences between DRX and eDRX.

*Fig 2. DRX vs eDRX comparison*
For CAT M technology in a test environment scenario the following command will set PTW=20,48 and eDRXcycle=81,92 (see AT command user guide for detailed definition)

at#cedrxs=1,4,"0101","1111"

Regarding the current profile for eDRX and ME910 it has to be said that the sleep current between paging occurrence and during the long sleep has a value of around 0,8 mA so when eDRX is used in combination with AT+CFUN=5 or 0 that allows to achieve average current consumption values less than 1mA in most cases.

eDRX is a different procedure in respect PSM that is practically OFF when PSM is activated but has to pay in terms of consumption to wakeup from PSM because a BOOT+CAMP+ TAU is needed.

That means that there is a breakeven point that suggest to use eDRX for applications that requires the module to be available very often and in any case at maximum every few hours, if the module can sleep more time the PSM feature must be evaluated because it could be more efficient from power consumption point of view.

**Note.** PSM and eDRX are not mutually exclusive and can work togheter. If PSM and eDRX are applied at the same time eDRX will basically work during the PSM idle time reducing the power consumption within the T3324.
5. HARDWARE CONTROLS

5.1. ME910C1 pins related to PSM Mode

<table>
<thead>
<tr>
<th>Pin</th>
<th>Signal</th>
<th>I/O</th>
<th>Function</th>
<th>Type</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>R12</td>
<td>ON_OFF/WAKE*</td>
<td>I</td>
<td>Input command for power ON and to wake from deep sleep mode</td>
<td>Digital</td>
<td>Active low, connected to open drain or open collector</td>
</tr>
<tr>
<td>R11</td>
<td>VAUX</td>
<td>O</td>
<td>1.8V LDO output</td>
<td>Supply 1.8V</td>
<td></td>
</tr>
<tr>
<td></td>
<td>PWRMON</td>
<td></td>
<td>Power ON monitor</td>
<td></td>
<td></td>
</tr>
<tr>
<td>R13</td>
<td>HW_SHUTDOWN*</td>
<td>I</td>
<td>HW Unconditional Shutdown</td>
<td>Digital</td>
<td>Active low, connected to open drain or open collector</td>
</tr>
</tbody>
</table>

5.2. ML865C1 pins related to PSM Mode

<table>
<thead>
<tr>
<th>Pin</th>
<th>Signal</th>
<th>I/O</th>
<th>Function</th>
<th>Type</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>7</td>
<td>WAKE</td>
<td>I</td>
<td>Input command for power ON and to wake from deep sleep mode</td>
<td>Digital</td>
<td>Active high, weak internal pull-down</td>
</tr>
<tr>
<td>51</td>
<td>VAUX</td>
<td>O</td>
<td>1.8V LDO output</td>
<td>Supply 1.8V</td>
<td></td>
</tr>
<tr>
<td></td>
<td>PWRMON</td>
<td></td>
<td>Power ON monitor</td>
<td></td>
<td></td>
</tr>
<tr>
<td>55</td>
<td>HW_SHUTDOWN*</td>
<td>I</td>
<td>HW Unconditional Shutdown</td>
<td>Digital</td>
<td>Active low, connected to open drain or open collector</td>
</tr>
</tbody>
</table>

5.3. CONTROL PINS DESCRIPTION

5.3.1. ON_OFF*/WAKE* (ME910C1)

ON_OFF*/WAKE* is the pin that turns on the system after VBATT and VBATT_PA is applied to ME910C1. Moreover, this pin can make an asynchronous wakeup of the system from the PSM Mode, before the scheduled event of timer T3412 expired.

To make asynchronous exit from PSM mode ON_OFF*/WAKE* pin must be set LOW for at least 5 seconds.
NOTE:

Don't use any pull up resistor on the ON_OFF*/WAKE* line, it is internally pulled up. Using pull up resistor may bring to latch up problems on the ME910C1 power regulator and improper power on/off of the module. The line ON_OFF*/WAKE* must be connected only in open collector or open drain configuration.

5.3.2. WAKE (ML865C1)
WAKE line can make an asynchronous wakeup of the system from the PSM Mode, before the scheduled event of timer T3412 expired.
To make asynchronous exit from PSM mode WAKE pin must be set HIGH for at least 5 seconds. In all other conditions WAKE pin must be set LOW.

NOTE:

WAKE line is active high (1.8V), and there is a weak internal pull-down (about 200K).

5.3.3. VAUX/PWRMON
There is no pin dedicated to PSM status indicator, host can only detect deep sleep mode by monitoring of VAUX/PWRMON output pin.

5.3.4. HW_SHUTDOWN*
During PSM mode, HW_SHUTDOWN toggle has no effect. The use of HW_SHUTDOWN* pin is valid only when ME910C1 has VAUX/PWRMON output HI.
5.4. SIM interface

SIM interface is powered down when ME910C1 enters in PSM mode to ensure minimal power consumption.
For this reason SIM PIN, if enabled, should be managed in every scheduled wake, or can simply be disabled.

5.5. PSM configuration

PSM has to be configured by the command AT+CPSMS.
The command controls whether the UE wants to apply PSM or not, as well as the requested extended periodic RAU value and the requested GPRS READY timer value in GERAN/UTRAN, the requested extended periodic TAU value in E-UTRAN and the requested Active Time value.
Examples:
AT+ CPSMS=0 → disable the use of PSM
AT+CPMS= 1,,"01100001","01100010" → PSM Mode is set to enabled and module enters in PSM after a minute (T3324 = 33) and stay in this mode for two minute (T3412 = 162).
For additional details on AT+CPMS command please refer to the ME910C1 AT commands Reference Guide

When Periodic Update Timer expires (T3324), ME910C1 turns off until the next scheduled wake-up time.
5.6. Hardware application example

WARNING:
If MCU and its digital interface has 1.8V supply, UART can be directly connected but all MCU output lines must be set to 0V in OFF and in PSM state to avoid backpowering.
WARNING:
If MCU and its digital interface has 1.8V supply, UART and WAKE can be directly connected but all MCU output lines must be set to 0V in OFF and in PSM state to avoid backpowering. MCU_GPIO01 must be totem pole type.
### 6. GLOSSARY AND ACRONYMS

<table>
<thead>
<tr>
<th>Description</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>TTSC</td>
<td>Telit Technical Support Centre</td>
</tr>
<tr>
<td>USB</td>
<td>Universal Serial Bus</td>
</tr>
<tr>
<td>HS</td>
<td>High Speed</td>
</tr>
<tr>
<td>DTE</td>
<td>Data Terminal Equipment</td>
</tr>
<tr>
<td>LTE</td>
<td>Long Term Evolution</td>
</tr>
<tr>
<td>PSM</td>
<td>Power Saving Mode according to 3GPP Rel.12</td>
</tr>
<tr>
<td>AS</td>
<td>Access Stratum</td>
</tr>
<tr>
<td>NAS</td>
<td>Non-Access Stratum</td>
</tr>
<tr>
<td>RAU</td>
<td>Routing Area Update</td>
</tr>
<tr>
<td>TAU</td>
<td>Tracking Area Update</td>
</tr>
<tr>
<td>HSIC</td>
<td>High Speed Inter Chip</td>
</tr>
<tr>
<td>SIM</td>
<td>Subscriber Identification Module</td>
</tr>
<tr>
<td>SPI</td>
<td>Serial Peripheral Interface</td>
</tr>
<tr>
<td>ADC</td>
<td>Analog – Digital Converter</td>
</tr>
<tr>
<td>DAC</td>
<td>Digital – Analog Converter</td>
</tr>
<tr>
<td>I/O</td>
<td>Input Output</td>
</tr>
<tr>
<td>GPIO</td>
<td>General Purpose Input Output</td>
</tr>
<tr>
<td>CMOS</td>
<td>Complementary Metal – Oxide Semiconductor</td>
</tr>
<tr>
<td>CLK</td>
<td>Clock</td>
</tr>
<tr>
<td>MRDY</td>
<td>Master Ready</td>
</tr>
<tr>
<td>SRDY</td>
<td>Slave Ready</td>
</tr>
<tr>
<td>CS</td>
<td>Chip Select</td>
</tr>
<tr>
<td>RTC</td>
<td>Real Time Clock</td>
</tr>
<tr>
<td>PCB</td>
<td>Printed Circuit Board</td>
</tr>
<tr>
<td>ESR</td>
<td>Equivalent Series Resistance</td>
</tr>
<tr>
<td>VSWR</td>
<td>Voltage Standing Wave Radio</td>
</tr>
<tr>
<td>VNA</td>
<td>Vector Network Analyzer</td>
</tr>
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</table>
7. DOCUMENT HISTORY

<table>
<thead>
<tr>
<th>Revision</th>
<th>Date</th>
<th>Changes</th>
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</thead>
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<tr>
<td>0</td>
<td>2018-02-08</td>
<td>First Issue</td>
</tr>
<tr>
<td>1</td>
<td>2019-03-04</td>
<td>Updated Applicability table and Paragraph 3. Added Par.4. Added ML865C1</td>
</tr>
</tbody>
</table>
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