



LE910Cx mPCIe

HW Release notes

80576DSW10190A Rev. 1 – 2018-10-16

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DOCUMENTATION

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APPLICABILITY TABLE

PRODUCTS

- ■ LE910CX MPCIE ADAPTOR V1 VARIANTS BASED ON CS1902C
- ■ LE910CX MPCIE ADAPTOR V2 VARIANTS BASED ON CS1902D

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1. INTRODUCTION

1.1. Scope

This document describes the hardware changes introduced between V1 and V2 of LE910Cx mPCIe adaptor related products.

1.2. Audience

This document is intended for Telit customers and system integrators, about to implement their applications using the LE910Cx mPCIe based products.

1.3. Contact Information, Support

For general contact, technical support services, technical questions and report documentation errors contact Telit Technical Support at:

- TS-EMEA@telit.com
- TS-AMERICAS@telit.com
- TS-APAC@telit.com
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Alternatively, use:

<http://www.telit.com/support>

For detailed information about where you can buy the Telit modules or for recommendations on accessories and components visit:

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Our aim is to make this guide as helpful as possible. Keep us informed of your comments and suggestions for improvements.

Telit appreciates feedback from the users of our information.

1.4. Text Conventions



Danger – This information **MUST** be followed or catastrophic equipment failure or bodily injury may occur.



Caution or Warning – Alerts the user to important points about integrating the module, if these points are not followed, the module and end user equipment may fail or malfunction.



Tip or Information – Provides advice and suggestions that may be useful when integrating the module.

All dates are in ISO 8601 format, i.e. YYYY-MM-DD.

2. LE910Cx mPCIe HW changes

LE910Cx mPCIe V2 design which is based on CS1902D introduces few changes and improvements compared to the previous V1 version. The following sections will describe those changes in details.

2.1. Feature list changes

The following items were changed between V1 and V2:

- SIM holder mounting option was removed
- eSIM mounting option was removed
- Large thermal pad was added on bottom side

2.2. Bottom view

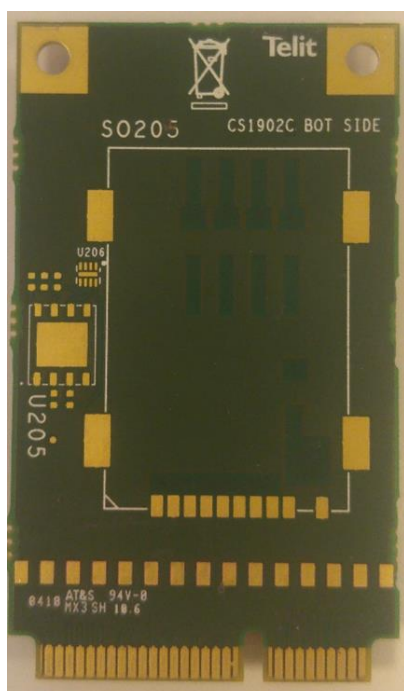


Figure 1 LE910Cx mPCIe V1



Figure 2 LE910Cx mPCIe V2



NOTE:

There is no difference in TOP view between V1 and V2.

2.3. Pinout related changes

The below pinout change was introduced in order to align this product PCM related pinout with other Telit mPCIe products and to align as well with other common designs available in the market.

mPCIe pin	V1 function	V2 function
47	PCM_RX	PCM_TX
49	PCM_TX	PCM_RX

2.4. Thermal improvement related changes

LE910Cx mPCIe V2 provides improved thermal performance compared to the V1 based design.

A large GND/Thermal pad was added on bottom side (as shown in Figure 2) and all heat sources thermal paths were linked to this GND/Thermal pad.

The new GND/Thermal pad enables the end user to place a thermal conductive pad between the LE910Cx mPCI bottom side and the carrier board top side hence conducting the heat of the mPCIe to a larger Thermal plane which resides on the main board.

Using this GND/Thermal pad on Telit platform has shown to improve the thermal performance thus allowing maximum throughput and maximum TX power at higher temperature without reaching the thermal mitigation threshold.

3. DOCUMENT HISTORY

Revision	Date	Changes
1	2018-10-14	First issue



SUPPORT INQUIRIES

Link to www.telit.com and contact our technical support team for any questions related to technical issues.

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